

FIG. 1B



FIG. 2

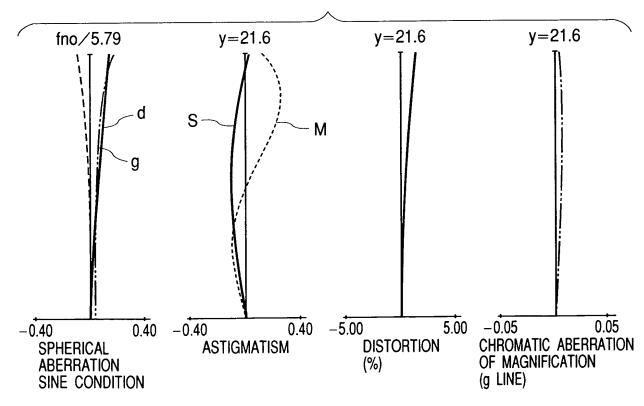


FIG. 3

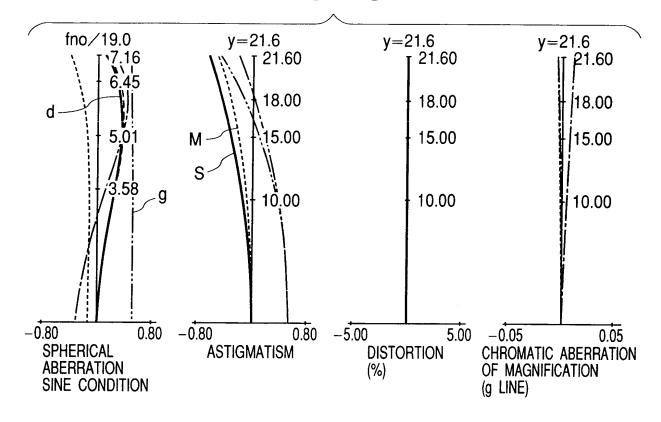


FIG. 4A

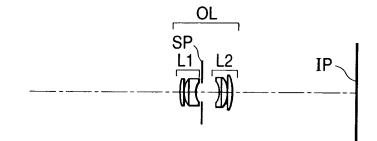
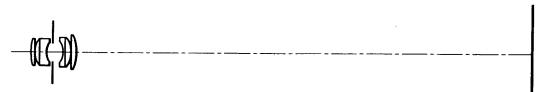


FIG. 4B



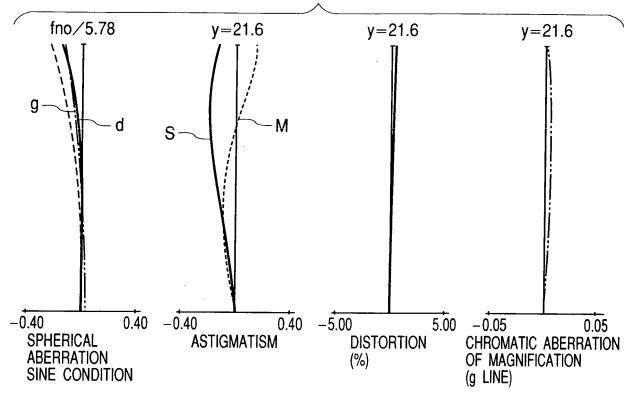
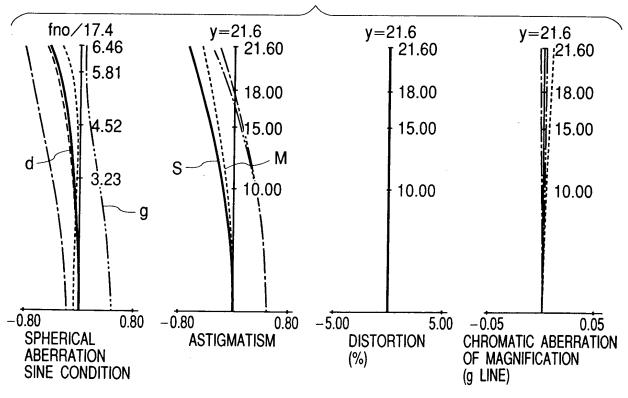
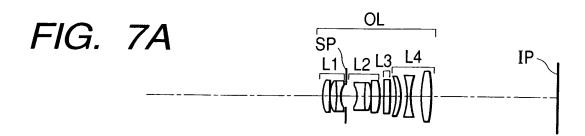
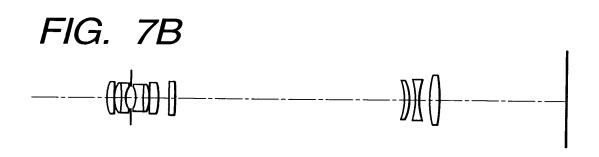


FIG. 6









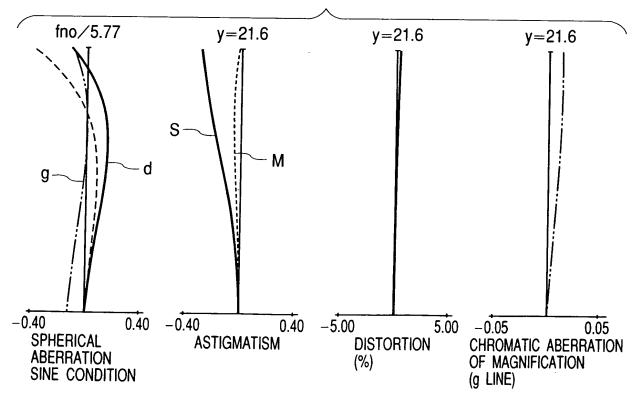


FIG. 9

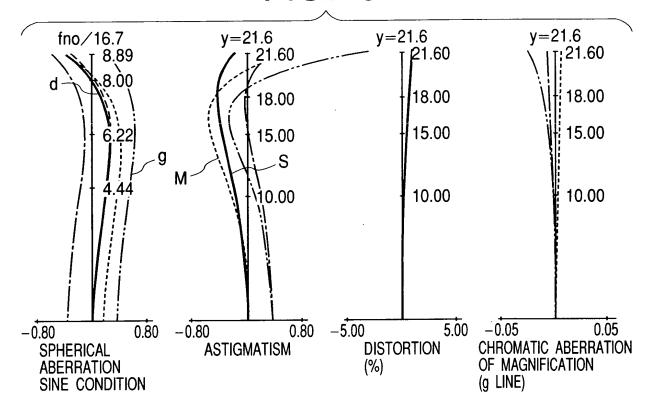
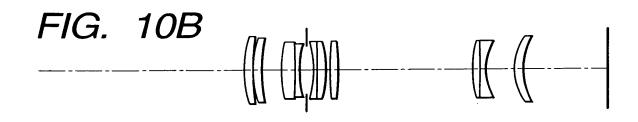
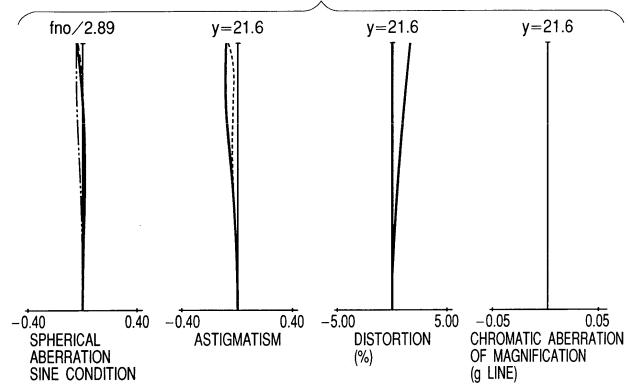
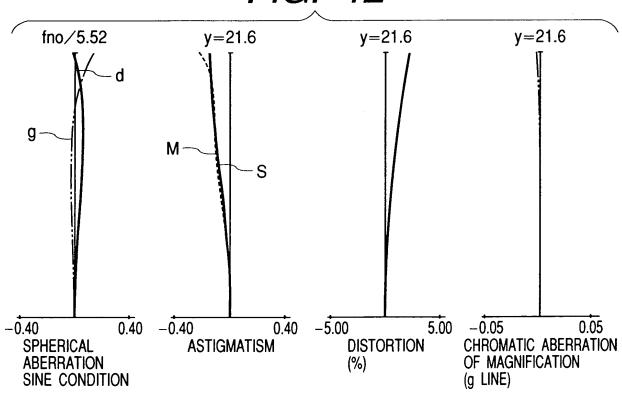
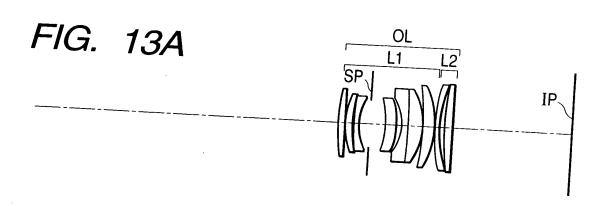


FIG. 10A L1 L2 IP









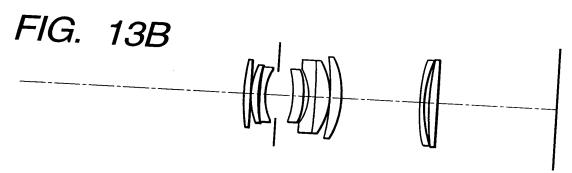
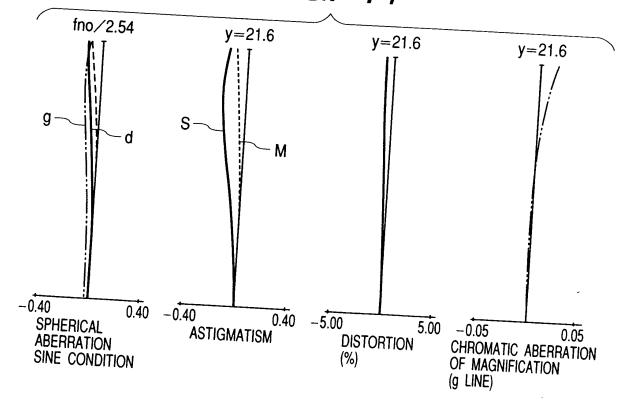
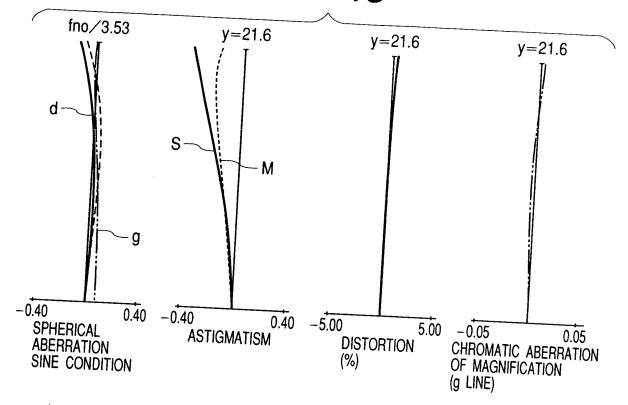
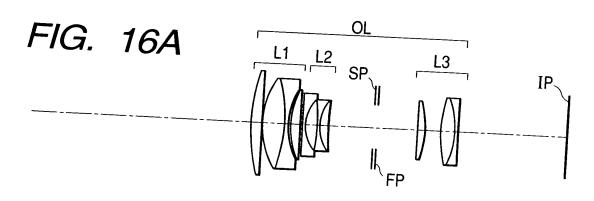
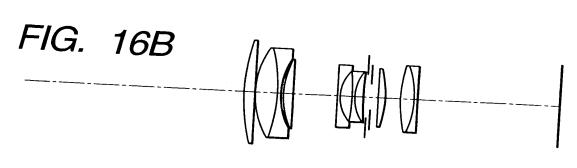


FIG. 14









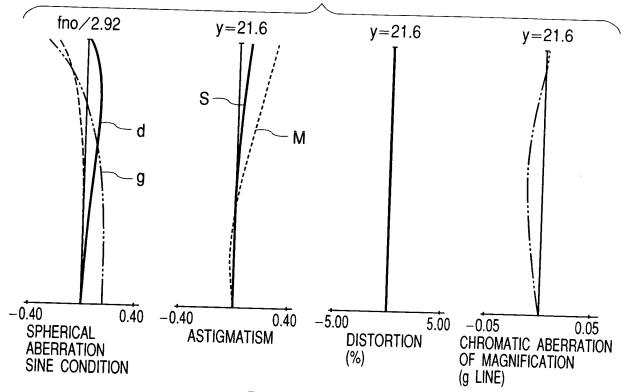
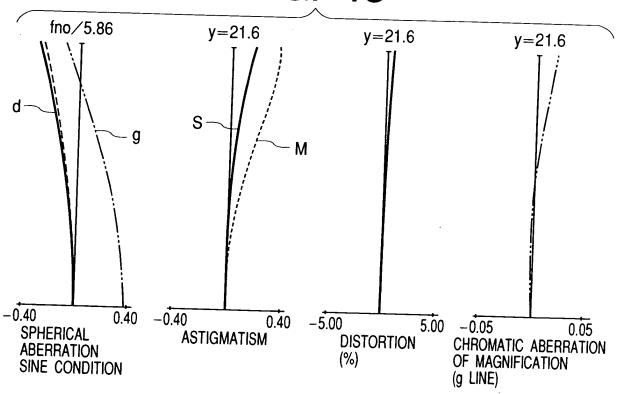
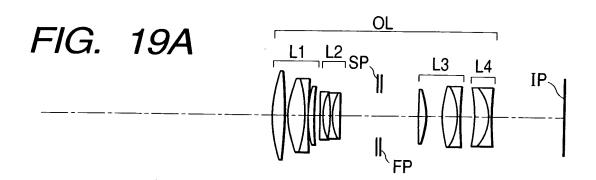


FIG. 18





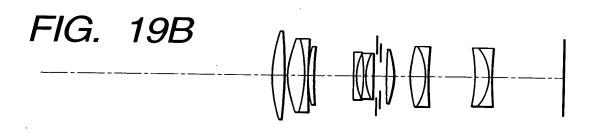
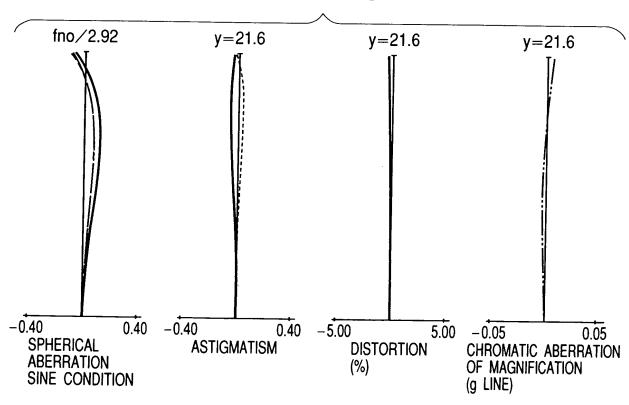
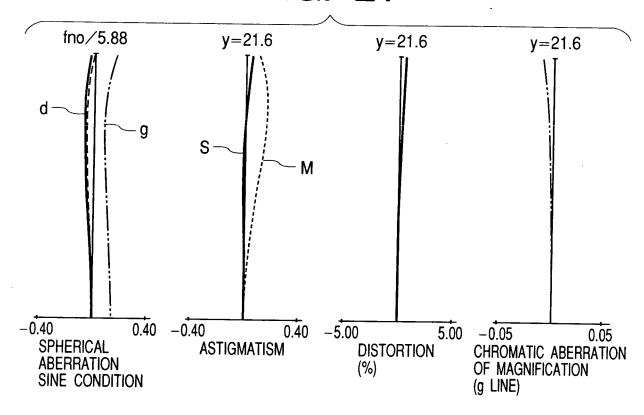


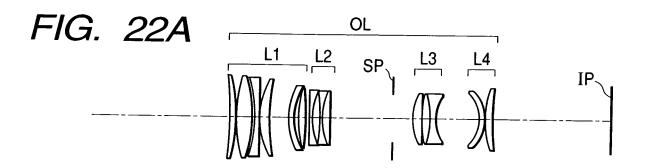
FIG. 20

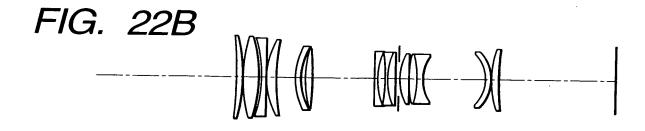


11 / 15

FIG. 21







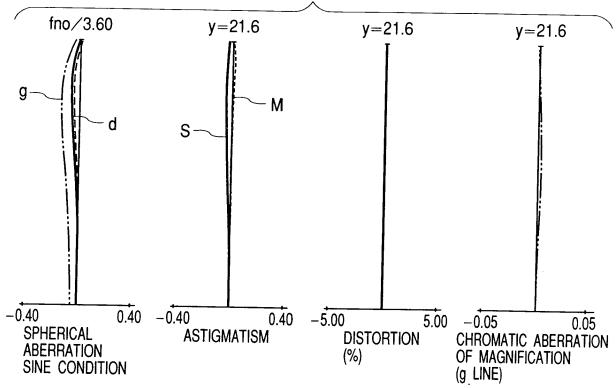


FIG. 24

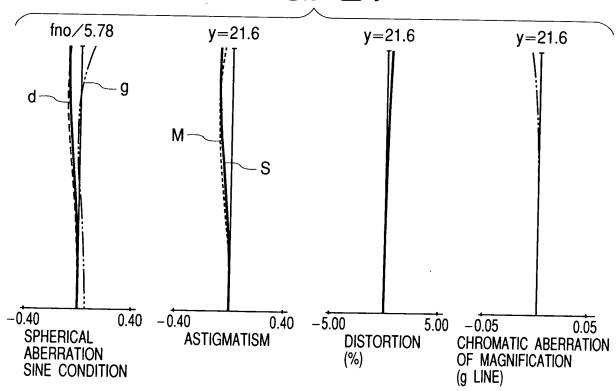


FIG. 25

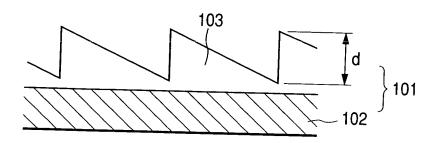


FIG. 26

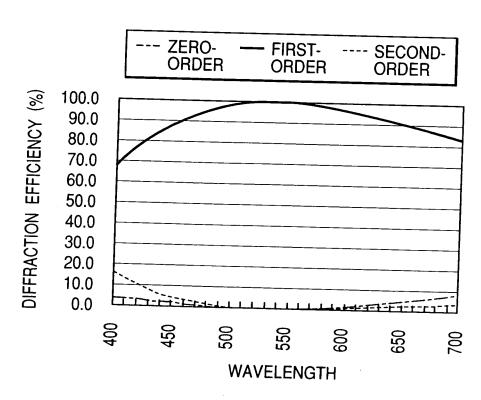


FIG. 27

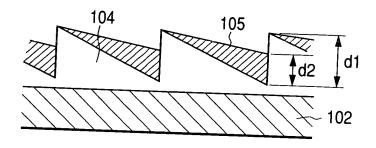
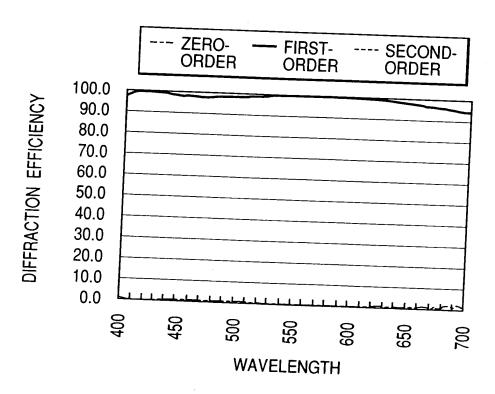


FIG. 28





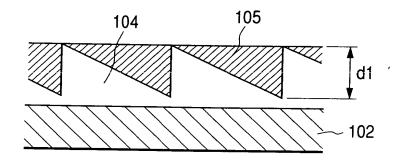


FIG. 30

